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## APPLICANTS

Yasunobu Umemoto, Kanagawa-ken, JAPAN;

Toshikazu Sei, Kanagawa-ken, JAPAN;

Toshiki Morimoto, Kanagawa-ken, JAPAN; Hiroaki Suzuki, Kanagawa-ken, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/819,532 03/27/2001 PAT 6,690,073

*- verified*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-87679 03/27/2001

*verified*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/16/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 2
Verified and Acknowledged	<i>[Signature]</i> Examiner's Signature	<i>sm</i> Initials			

## ADDRESS

26379  
 GRAY CARY WARE & FREIDENRICH LLP  
 2000 UNIVERSITY AVENUE  
 E. PALO ALTO, CA  
 94303-2248

## TITLE

Semiconductor integrated circuit making use of standard cells

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